### FreeBSD SMPng: Behind the scenes

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Sydney, 27 September 2001



• How we got into this mess.



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- Why the UNIX kernel is not suited to multiple processors.



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- Why the UNIX kernel is not suited to multiple processors.
- Solving the problem.



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- Current state of play.



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- Why the UNIX kernel is not suited to multiple processors.
- Solving the problem.
- Team dynamics.
- Current state of play.
- Looking forward.



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- One of the problems was the "big kernel lock" SMP implementation.



• One CPU



- One CPU
- Processes perform user functions.



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16

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- Processes perform user functions.
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- Interrupt handlers have priority over processes.



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- Processes are not scheduled while running kernel code.



### Interrupts

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- Different synchronization: block interrupts in critical kernel code.



### Interrupts

- Interrupts cannot be delayed until kernel is inactive.
- Different synchronization: block interrupts in critical kernel code.
- Finer grained locking: splbio for block I/O, spltty for serial I/O, splnet for network devices, etc.



#### Problems on SMP machines

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- UNIX approach doesn't allow more than one process running in kernel mode.
- "Solution": introduce Big Kernel Lock. Spin (loop) waiting for this lock if it's taken.
- Disadvantage: much CPU time may be lost.



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- FreeBSD people looked at the Linux solution and didn't under hh h h h h h h h like it.
- BSDi bought out Walnut Creek CDROM.



## The BSD/OS merge

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- FreeBSD developers allowed to merge "significant parts" of BSD/OS code.
- Decision made to merge SMPng.

## The meeting at Yahoo!

- Interested developers met at Yahoo! in June 2000.
- Total of 20 participants.
- 11 FreeBSD developers.
- 3 Apple developers.
- 3 Yahoo! staff.
- 2 BSDi developers.



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- Create "fine-grained" locking: lock only small parts of the kernel.
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- Problem: interrupt handlers can't block.
- Solution: let them block, then.



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- Short term: normal processes, involve scheduler overhead on every invocation.
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- Choice dictated by stability requirements during changeover.
- Resurrect the idle process, which gives a process context to each interrupt process.

```
USER
          PID %CPU
                     %MEM
                             VSZ
                                   RSS
                                             STAT
                                                   STARTED
                                                                  TIME COMMAND
               99.0
                                         ??
                                                   Thu10AM 4332:57.28
                                                                          (idle: cpu1)
root
                      0.0
                               0
                                     0
                                             RL
root
           11
               99.0
                      0.0
                               0
                                     0
                                         ??
                                             RL
                                                   Thu10AM 4331:26.95
                                                                          (idle: cpu0)
           12
                                                   Thu10AM
                                                               6:02.04
                                                                         (swil: net)
root
                0.0
                      0.0
                               0
                                         ??
                                             WT.
           13
                               0
root
                0.0
                      0.0
                                         ??
                                             WL
                                                   Thu10AM
                                                              11:43.06
                                                                         (swi6: ttv:sio clock)
                               0
                                         ??
                                                               0:00.00
                                                                         (swi4: vm)
           14
                0.0
                      0.0
                                     0
                                             WT.
                                                   Thu10AM
root
           15
                0.0
                      0.0
                               0
                                         ??
                                                   Thu10AM
                                                               0:00.00
                                                                         (swi2:
root
                                     0
                                             WL
                                                                                 camnet)
root
           16
                0.0
                      0.0
                               0
                                     0
                                         ??
                                             WL
                                                   Thu10AM
                                                               0:00.00
                                                                         (swi3: cambio)
           17
                0.0
                      0.0
                               0
                                                               0:00.00
                                                                         (swi5: task queue)
root
                                         ??
                                             WL
                                                   Thu10AM
           18
                0.0
                               0
                                         ??
                                                               0:00.16
                                                                         (irg14: ata0)
root
                      0.0
                                     0
                                             WL
                                                   Thu10AM
           19
                0.0
                      0.0
                               0
                                         ??
                                                               0:00.00
                                                                         (irg15: ata1)
root
                                     0
                                             WL
                                                   Thu10AM
                                                              10:38.31
           20
                                         ??
                                                                         (irq9: dc0)
                0.0
                      0.0
                                             WL
                                                   Thu10AM
root
root
           21
                0.0
                      0.0
                               0
                                         ??
                                             WL
                                                   Thu10AM
                                                               1:51.00
                                                                         (irgl1: atapcil+)
           22
                               0
                                         ??
                                                               0:00.00
                                                                         (irg1: atkbd0)
                0.0
                      0.0
                                     0
                                             WT.
                                                   Thu10AM
root
           23
                0.0
                      0.0
                               0
                                     0
                                         ??
                                                   Thu10AM
                                                               0:00.00
                                                                         (swi0:
root
                                             WL
                                                                                 ttv:sio)
           24
                               0
                                     0
                                         ??
                                                               0:00.00
                                                                                 sio0)
root
                0.0
                      0.0
                                             WL
                                                   Thu10AM
                                                                         (ira4:
           25
                0.0
                               0
                                                   Thu10AM
                                                               0:00.00
root
                      0.0
                                         ??
                                             WL
                                                                         (ira7: ppc0)
           26
                0.0
                      0.0
                               0
                                     0
                                         ??
                                             WL
                                                   Thu10AM
                                                               0:00.00
                                                                         (irg0: clk)
root
            27
                0.0
                               0
                                         ??
                                                   Thu10AM
                                                               0:00.00
                      0.0
                                     0
                                             WL
                                                                         (ira8: rtc)
root
                                         ??
                                                               0:02.33
root
                0.0
                      0.0
                                             DT.
                                                   Thu10AM
                                                                         (pagedaemon)
                               0
root
                0.0
                      0.0
                                         ??
                                             DL
                                                   Thu10AM
                                                               0:00.00
                                                                         (vmdaemon)
                               0
                                         ??
             4
                0.0
                      0.0
                                     0
                                             DL
                                                   Thu10AM
                                                               0:00.02
                                                                         (pagezero)
root
root
             5
                0.0
                      0.0
                               0
                                     0
                                         ??
                                             DL
                                                   Thu10AM
                                                               0:08.08
                                                                         (bufdaemon)
                               0
                                     0
                                         ??
                                                               2:17.24
root
             6
                0.0
                      0.0
                                             DL
                                                   Thu10AM
                                                                         (syncer)
                               0
                                     0
                                                               0:01.66
root
                0.0
                      0.0
                                         ??
                                             DLs
                                                   Thu10AM
                                                                         (swapper)
                0.0
                             656
                                    29
                                         ??
                                                               0:03.93
                                                                        /sbin/init -d
root
                      0.1
                                             ILs
                                                   Thu10AM
```

### Conclusions at Yahoo!

• Aim for stability, not performance, during the development.



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- Port BSDi code, don't reinvent the wheel.



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- Aim for stability, not performance, during the development.
- Port BSDi code, don't reinvent the wheel.
- Use heavy-weight threads at first to enable better debugging.



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- Jason Evans to be project manager.
- (But we never had a project manager before).



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- Matt rewrote the code.
- Code later ported by Jake Burkholder.



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- Greg Lehey started porting interrupt threads.
- Greg had two weeks to port the code.
- Porting the BSD/OS code was more difficult than anticipated.
- Heavyweight threads only in SPARC code.
- SPARC and Intel code very different.



### First steps

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- Four-way comparison: FreeBSD 4.0, BSD/OS 4.0, BSD/OS 5 Intel, BSD/OS 5 SPARC.



#### First steps

- Differences between FreeBSD and BSD/OS larger than anticipated.
- Four-way comparison: FreeBSD 4.0, BSD/OS 4.0, BSD/OS 5 Intel, BSD/OS 5 SPARC.
- Took two months.



#### Initial commit

```
From: Jason Evans < iasone@FreeBSD.org>
To: cvs-committers@FreeBSD.org, cvs-all@FreeBSD.org
Subject: cvs commit: src/bin/ps print.c src/share/man/man9 mutex.9 Makefile
         src/usr.bin/top machine.c src/sys/alpha/alpha mp_machdep.c
         synch machdep.c clock.c genassym.c interrupt.c ipl funcs.c
         locore.s machdep.c mem.c pmap.c prom.c support.s swtch.s trap.c ...
iasone
            2000/09/06 18:33:03 PDT
  (file names omitted)
 Loq:
 Major update to the way synchronization is done in the kernel. Highlights
  include:
  * Mutual exclusion is used instead of spl*(). See mutex(9). (Note: The
    alpha port is still in transition and currently uses both.)
  * Per-CPU idle processes.
  * Interrupts are run in their own separate kernel threads and can be
   preempted (i386 only).
 Partially contributed by: BSDi (BSD/OS)
  Submissions by (at least): cp, dfr, dillon, groq, jake, jhb, sheldonh
```

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- Much cosmetic work.
- Terminology problems: what is a "mutex"?
- No clear direction: Many contributors decided on their own locking constructs.
- Loss of Jason Evans as project leader in March 2001.
- No replacement project leader.



#### The kernel summit

• "Kernel summit" held at Boston USENIX conference on Saturday, 30 June 2001.



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#### The kernel summit

- "Kernel summit" held at Boston USENIX conference on Saturday, 30 June 2001.
- Many interruptions.
- Conclusion: release FreeBSD 5.0 in November 2001, ready or not.



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- Much traffic on IRC channel.



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- Much traffic on IRC channel.
- IRC tended to limit the number of participants.
- No record of discussions.



• BSD/OS supplied debugging tools.



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- Not all BSD/OS tools have been ported.



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- Not all BSD/OS tools have been ported.
- Few new tools have been written: debugging is not interesting enough.



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- Not all BSD/OS tools have been ported.
- Few new tools have been written: debugging is not interesting enough.
- Good task for "Junior Kernel Hacker".



#### Documentation

• Change logs very well documented.



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- Individual functions well documented.



#### **Documentation**

- Change logs very well documented.
- Individual functions well documented.
- Overall project plan less clear.



### Sample man page

```
MUTEX(9)
                       FreeBSD Kernel Developer's Manual
                                                                       MUTEX(9)
NAME
     mutex, mtx init, mtx lock, mtx lock spin, mtx lock flags,
     mtx lock spin flags, mtx trylock, mtx trylock flags, mtx unlock,
     mtx unlock spin, mtx unlock flags, mtx unlock spin flags, mtx destroy,
     mtx initialized, mtx owned, mtx recursed, mtx assert - kernel synchro-
     nization primitives
SYNOPSIS
     #include <svs/param.h>
     #include <sys/lock.h>
     #include <sys/mutex.h>
     biov
     mtx init(struct mtx *mutex, const char *description, int opts);
     biov
     mtx lock(struct mtx *mutex);
     biov
     mtx lock_spin(struct mtx *mutex);
     biov
     mtx lock flags(struct mtx *mutex, int flags);
```

#### Sample man page (continued)

#### DESCRIPTION

Mutexes are the most basic and primary method of process synchronization. The major design considerations for mutexes are:

- Acquiring and releasing uncontested mutexes should be as cheap as possible.
- 2. They must have the information and storage space to support priority propagation.
- 3. A process must be able to recursively acquire a mutex, provided that the mutex is initialized to support recursion.

There are currently two flavors of mutexes, those that context switch when they block and those that do not.

By default, MTX\_DEF mutexes will context switch when they are already held. As a machine dependent optimization they may spin for some amount of time before context switching. It is important to remember that since a process may be preempted at any time, the possible context switch introduced by acquiring a mutex is guaranteed to not break anything that isn't already broken.

## Sample commit log

jhb 2001/09/10 14:04:49 PDT

Modified files:

sys/kern kern\_shutdown.c

Log:

- Axe holding\_giant as it is not used now anyways and was ok'd by dillon in an earlier e-mail.
- We don't need to test the console right before we vfprintf() the panicstr message. The printing of the panic message is a fine console test by itself and doesn't make useful messages scroll off the screen or tick developers off in quite the same.

Requested by: jlemon, imp, bmilekic, chris, gsutter, jake (2)

Revision Changes Path

1.110 +5 -36 src/sys/kern/kern shutdown.c



From: John Baldwin <jhb@FreeBSD.org>

Date: Tue, 21 Nov 2000 13:10:15 -0800 (PST)

jhb 2000/11/21 13:10:15 PST

Modified files:

sys/kern kern\_ktr.c

Log:

Ahem, fix the disclaimer portion of the copyright so it disclaim's the voices in my head. You can sue the voices in Bill Paul's head all you want.

Noticed by: jhb

Revision Changes Path

1.6 +3 -3 src/sys/kern/kern\_ktr.c



```
--- kern_ktr.c 2000/11/15 21:51:53 1.5
+++ kern_ktr.c 2000/11/21 21:10:15 1.6
@@ -14,10 +14,10 @@
       may be used to endorse or promote products derived from this software
       without specific prior written permission.
- * THIS SOFTWARE IS PROVIDED BY Bill Paul AND CONTRIBUTORS ''AS IS'' AND
+ * THIS SOFTWARE IS PROVIDED BY JOHN BALDWIN AND CONTRIBUTORS 'AS IS' AND
  * ANY EXPRESS OR IMPLIED WARRANTIES, INCLUDING, BUT NOT LIMITED TO, THE
  * IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE
- * ARE DISCLAIMED. IN NO EVENT SHALL Bill Paul OR THE VOICES IN HIS HEAD
+ * ARE DISCLAIMED. IN NO EVENT SHALL JOHN BALDWIN OR THE VOICES IN HIS HEAD
  * BE LIABLE FOR ANY DIRECT, INDIRECT, INCIDENTAL, SPECIAL, EXEMPLARY, OR
  * CONSEQUENTIAL DAMAGES (INCLUDING, BUT NOT LIMITED TO, PROCUREMENT OF
  * SUBSTITUTE GOODS OR SERVICES; LOSS OF USE, DATA, OR PROFITS; OR BUSINESS
@@ -26.7 + 26.7 @@
  * ARISING IN ANY WAY OUT OF THE USE OF THIS SOFTWARE, EVEN IF ADVISED OF
  * THE POSSIBILITY OF SUCH DAMAGE.
- * $FreeBSD: src/sys/kern/kern ktr.c.v 1.5 2000/11/15 21:51:53 jhb Exp $
+ * $FreeBSD: src/sys/kern/kern ktr.c,v 1.6 2000/11/21 21:10:15 jhb Exp $
  * /
 /*
```

```
From: John Baldwin <jhb@FreeBSD.org>
On 21-Nov-00 John Baldwin wrote:
> jhb
        2000/11/21 13:10:15 PST
>
   Modified files:
>
      sys/kern
                          kern ktr.c
>
   Log:
>
   Ahem, fix the disclaimer portion of the copyright so it disclaim's the
   voices in my head. You can sue the voices in Bill Paul's head all you
>
   want.
>
>
   Noticed by: jhb
>
Oh geez. That should be 'Noticed by: jlemon'. I guess the voices
```

are getting a bit too rambunctious.

From: Warner Losh <imp@village.org>

In message <XFMail.001121131818.jhb@FreeBSD.org> John Baldwin writes:
 Oh geez. That should be 'Noticed by: jlemon'. I guess the voices: are getting a bit too rambunctious.

It could be worse. You could be talking about yourself in the third person. Warner hates it when he does that.



From: John Baldwin <jhb@FreeBSD.ORG>

On 21-Nov-00 Warner Losh wrote:

- > In message <XFMail.001121131818.jhb@FreeBSD.org> John Baldwin writes:
- >: Oh geez. That should be 'Noticed by: jlemon'. I guess the voices are
- >: getting
- >: a bit too rambunctious.

>

- > It could be worse. You could be talking about yourself in the third
- > person. Warner hates it when he does that.

Well, I'm sure Warner will have a private discussion with Warner about doing that in public.

I wonder how the voices do their locking...



Warner will do that only if Warner notices.

From: Warner Losh <imp@village.org>

: I wonder how the voices do their locking...

Warner Speculates that Warner's voices don't do locking

```
From: John Baldwin <jhb@FreeBSD.ORG>
On 21-Nov-00 Warner Losh wrote:
> In message <XFMail.001121133952.jhb@FreeBSD.org> John Baldwin writes:
>: Well, I'm sure Warner will have a private discussion with Warner about
>: doing that in public.
>
> Warner will do that only if Warner notices.
>
>: I wonder how the voices do their locking...
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> Warner Speculates that Warner's voices don't do locking.
```

John thinJohn's voices are too inks that the consefficient to useole driver doesn't ha sleep locks and endve any lock up sping yeinning a lott.

fatal double fault

eip = 0x000000

ebp = %62F k epomn e

### (untangled)

John thinJohn's voices are too inks that the consefficient to useole driver doesn't ha sleep locks and endve any lock up sping yeinning a lott.

- 1. John thinks that the console driver doesn't have any locking yet.
- 2. John's voices are too efficient to use sleep locks and end up spinning a lot.



#### Performance

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- The release team decides to postpone the release by one year.



### Further information

http://www.FreeBSD.org/smp/

Join in! The FreeBSD project needs more clever hackers.

These slides are available at

http://www.lemis.com/SMPng/AUUG2001/

